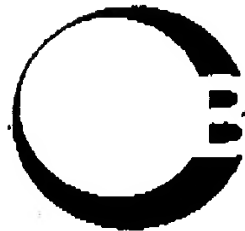


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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: Jaideep Jain et al. Serial No.: 09/677,087 Filed: September 29, 2000 Title: Operational List For Simultaneous Wafer Scheduling And System Event Scheduling	Examiner: Lee, Douglas S. Art Unit: 2125 Atty. Docket No.: 10001.000700 (NVLS 327)
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//

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In re Jaideep Jain et al.
U.S. Appl. No. 09/677,087

1

PATENT

Favorable action is respectfully solicited.

Respectfully submitted,

Jaideep Jain et al.

Dated: September 28, 2004

By: *Patrick D. Benedicto*
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/677,087	09/29/2000	2821	840	50544.000006	12	13	3

Patrick Benedicto
Squire Sanders & Dempsey LLP
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DATES ENTERED: _____

Applicant(s)

Jaideep Jain, San Jose, CA ;
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Eileen A.H. Wong, Cupertino, CA ;
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FEB 16 2001

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ATTORNEY

SQUIRE, SANDERS & DEMPSEY

Continuing Data as Claimed by Applicant

This appln claims benefit of 60/157,253 10/01/1999
Foreign Applications

If Required, Foreign Filing License Granted 11/21/2000

Title

Operational lists for simultaneous wafer scheduling and system event scheduling

Preliminary Class

438

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